

Docket No.: 067161-0117



PATENT

DFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	Customer Number: 20277
Shinji TANAKA	:	Confirmation Number: 8196
Application No.: 10/682,153	:	Group Art Unit: 2824
Filed: October 10, 2003	:	Examiner: Hien N. Nguyen

For: SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA AND
PARITY DATA IN BURST OPERATION

STATUS INQUIRY

Mail Stop Status Inquiry
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The above-identified application was filed on October 10, 2003. Since more than one and one-half years have elapsed since that date, and no action has been received by attorney for applicant, Applicant respectfully requests that the undersigned attorney be informed of the status of the referenced application. It is noted that the Official Gazette indicates applications filed in the above-assigned Group Art Unit on or before November 5, 2003 should have received an action on the merits.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


Stephen A. Becker

Registration No. 26,527

**Please recognize our Customer No. 20277
as our correspondence address.**

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 SAB:cms
Facsimile: 202.756.8087
Date: April 15, 2005